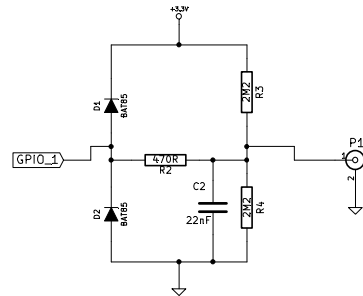
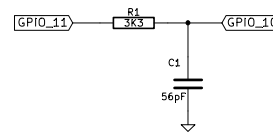
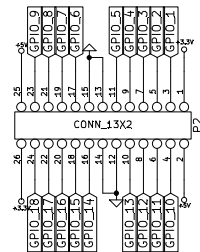


# FleaFPGA Simple Oscilloscope example – Test Circuit

Note: Keep R1 & C1 as close to the GPIO header as possible!  
C1 may need to be altered by a few pF to obtain best possible signal linearity

GPIO HEADER  
(As seen from top of PCB)



Scope Probe Input (Note 10x mode not supported)  
Input voltage range 0 -> 3.3V DC or AC signal  
R3 & R4 allow biasing of AC signals to 1/2 Vcc

Drawn: Valentin Angelovski		
File: temp.sch		
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